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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Berger, Alexander J.; Kretz, Frank E.  
Assignee: Tru-Si Technologies, Inc.  
Title: Alignment Of Semiconductor Wafers and Other Articles  
Serial No.: Not Yet Known Filing Date: Herewith  
Examiner: Not Yet Known Group Art Unit: Not Yet Known  
Docket No.: M-11882 US

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09/905218  
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San Jose, California  
July 13, 2001

BOX PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant wishes to call to the attention of the Examiner the documents cited in the enclosed form PTO 1449 and the following documents:

1. U.S. Patent Application No. 09/632,236 Filed August 4, 2000;
2. U.S. Patent Application No. 09/633,086 Filed August 4, 2000;
3. U.S. Patent Application No. 09/877,366 Filed June 8, 2001; and
4. U.S. Patent Application No. 09/791,977 Filed February 22, 2001.

Copies of the documents cited on the PTO 1449 and hereinabove are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above ; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO:

EL 701 019 784 US

Respectfully submitted,

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